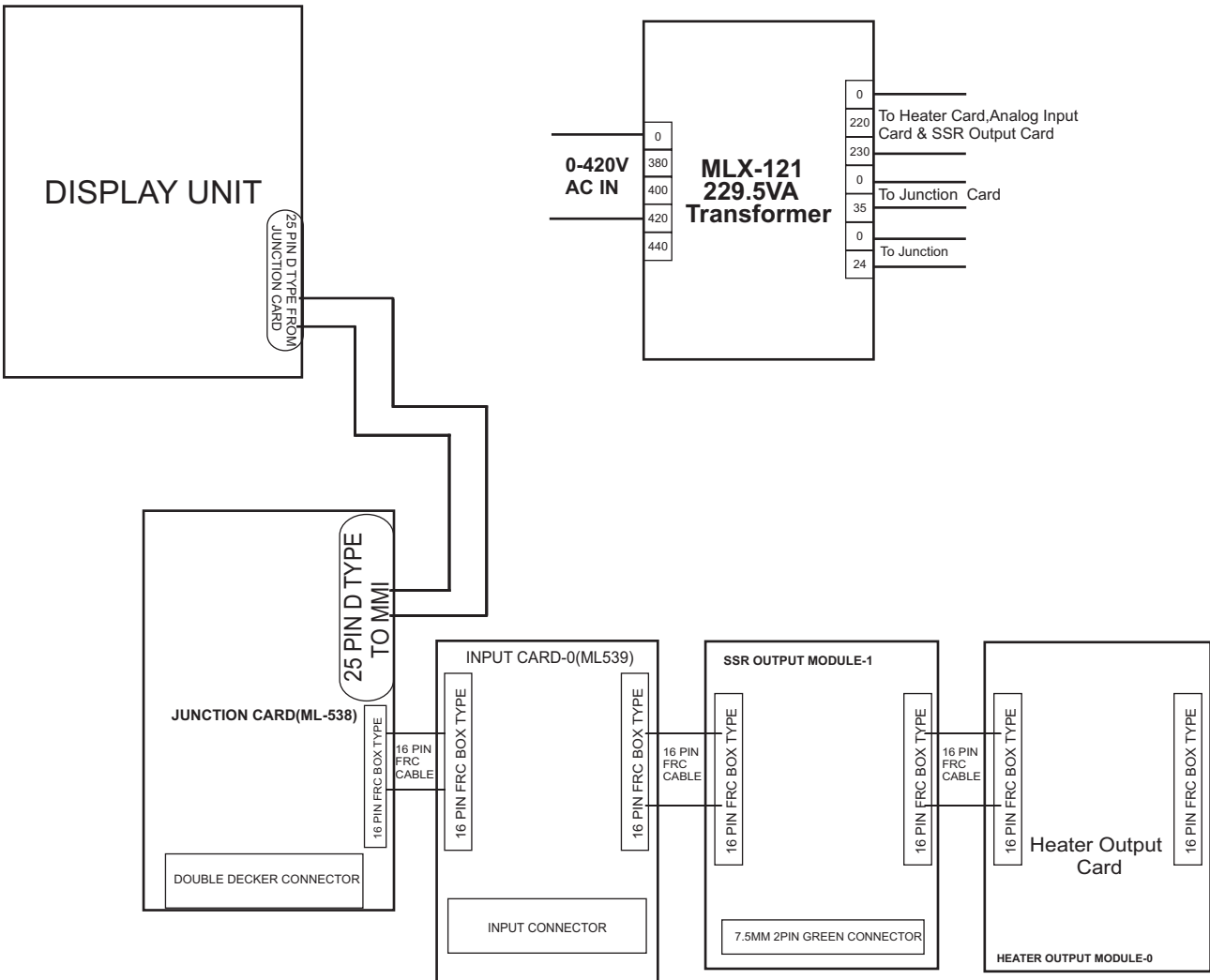
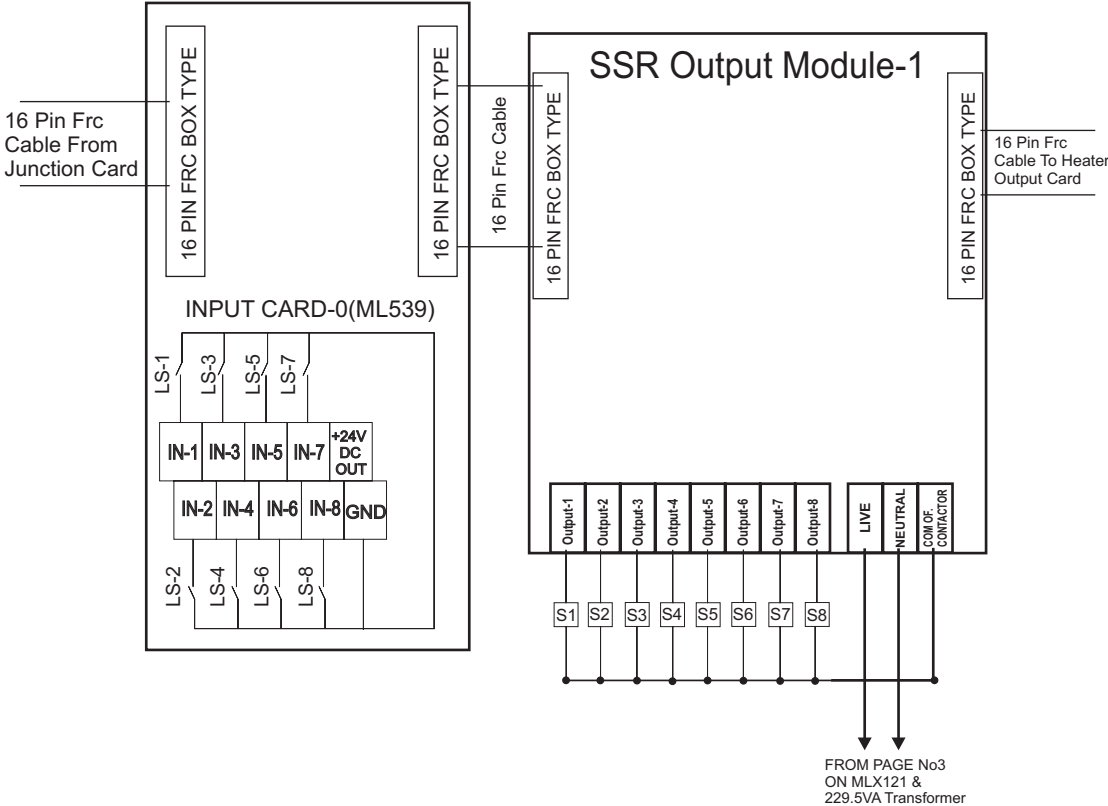


BLOCK DIAGRAM

BLOkon: __ /1.1/7/8,8,0,0,0,8/A



WIRING DIAGRAM INPUT/OUTPUT MODULES



WIRING DIAGRAM

